

Fig 1

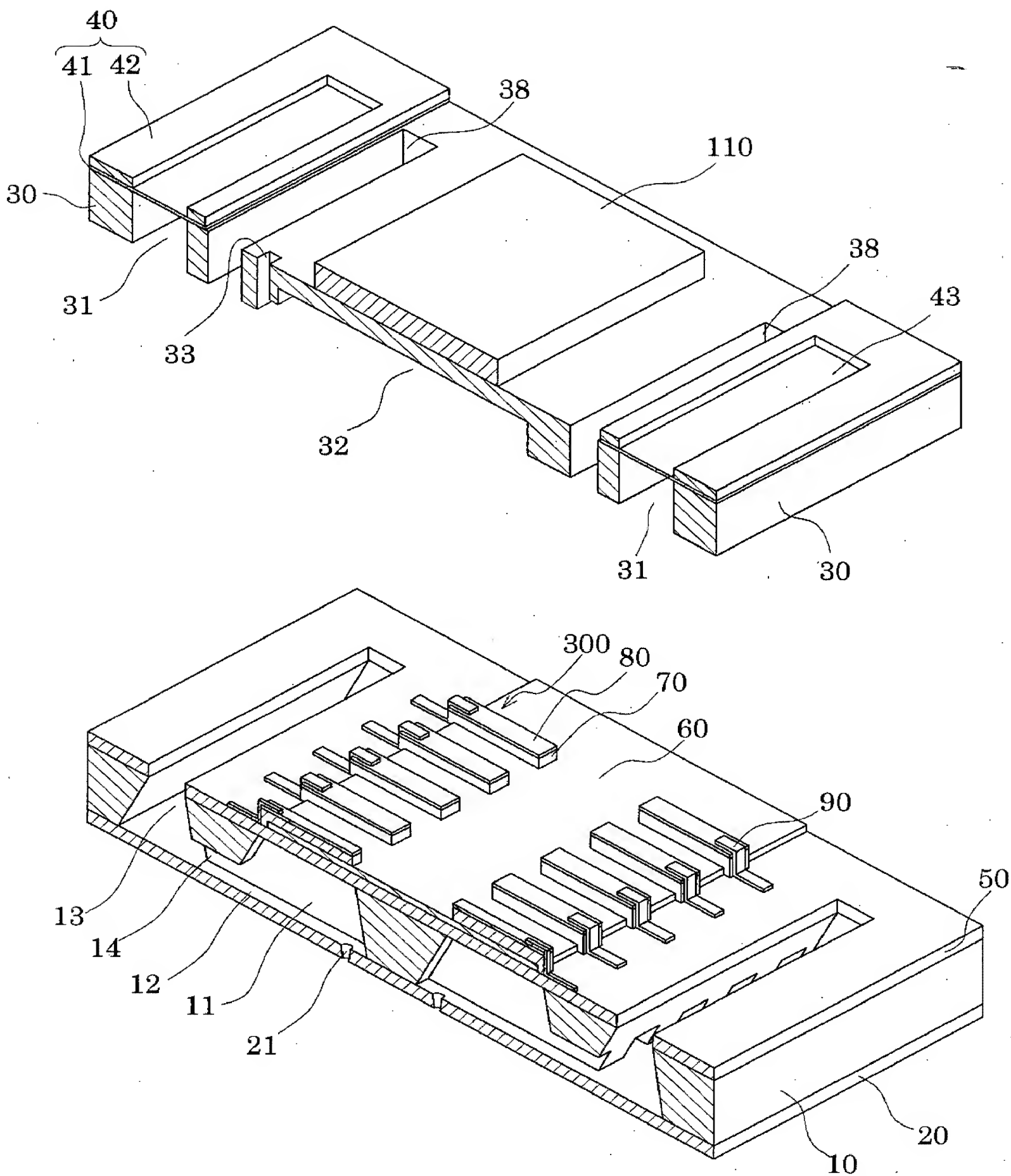


Fig 2A

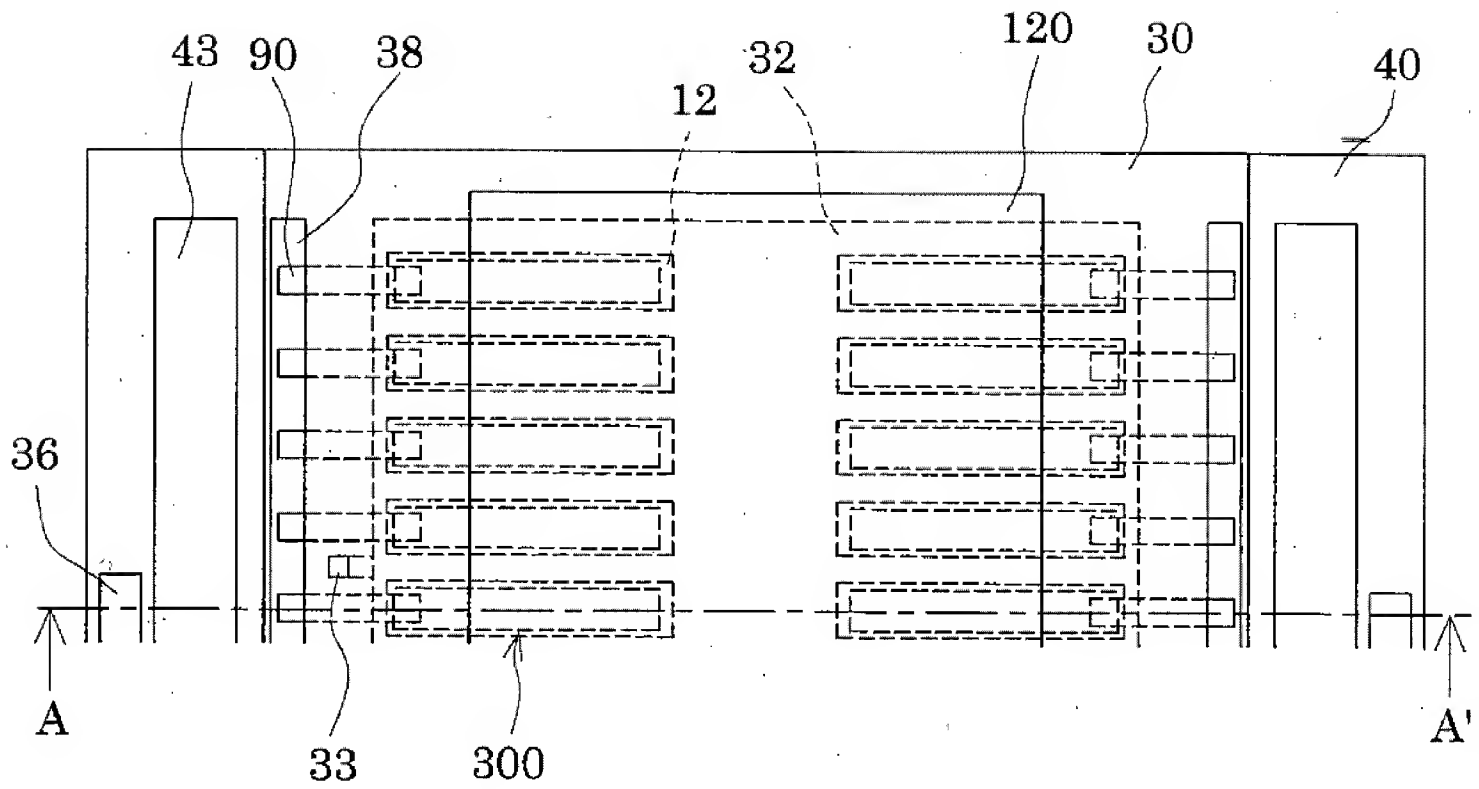


Fig 2B

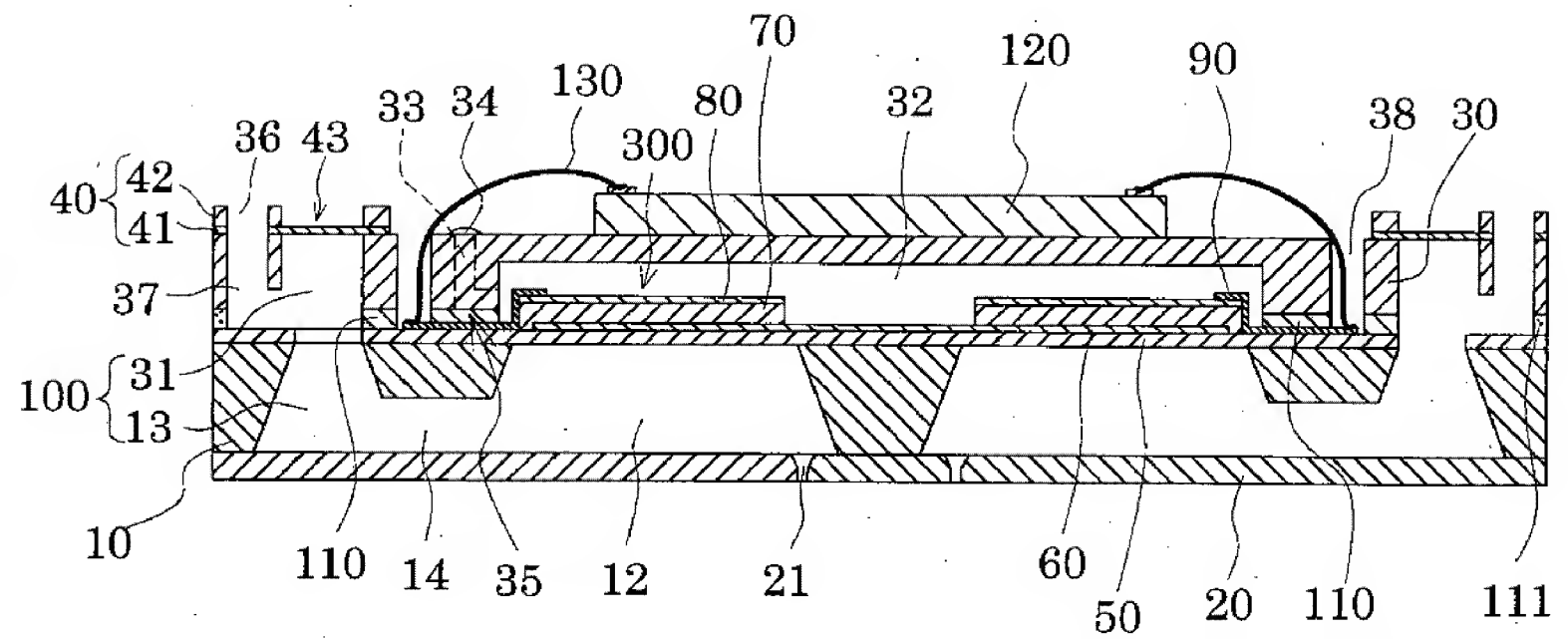


Fig 3A

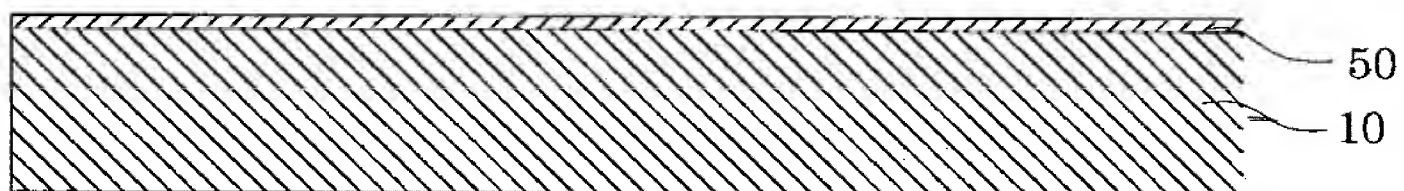


Fig 3B

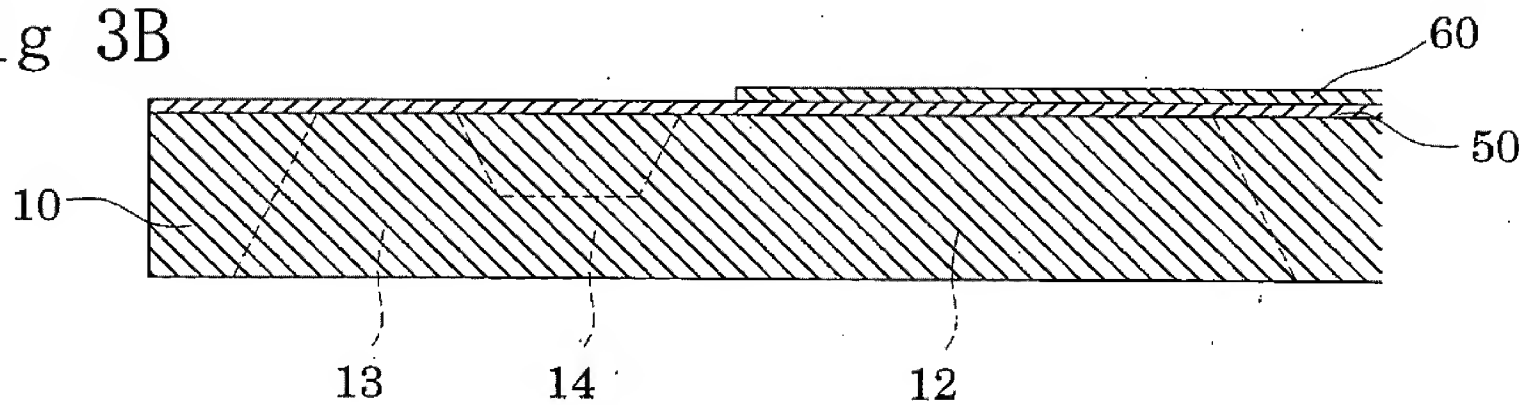


Fig 3C

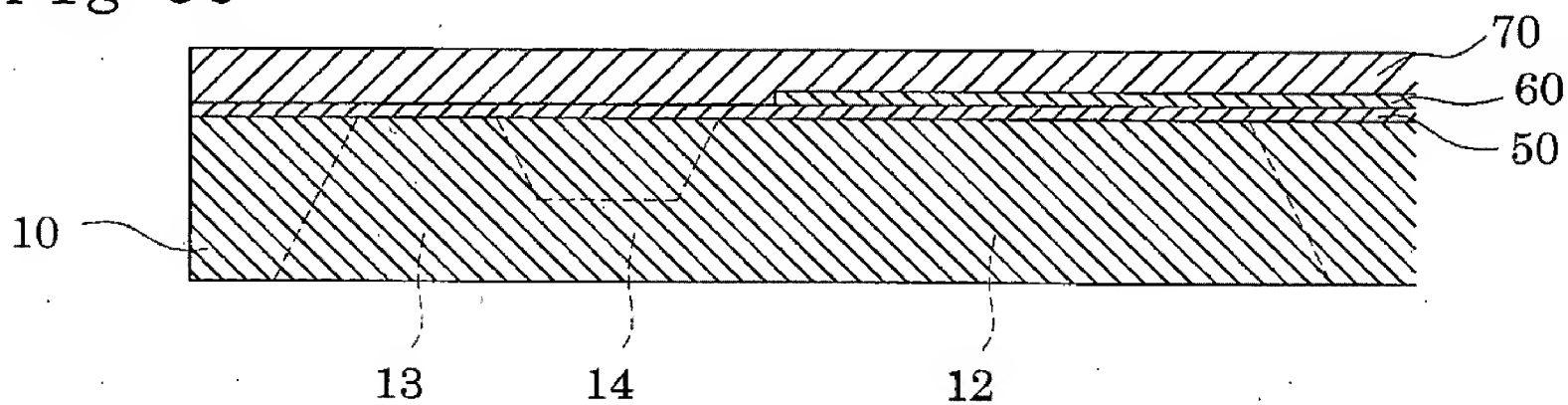
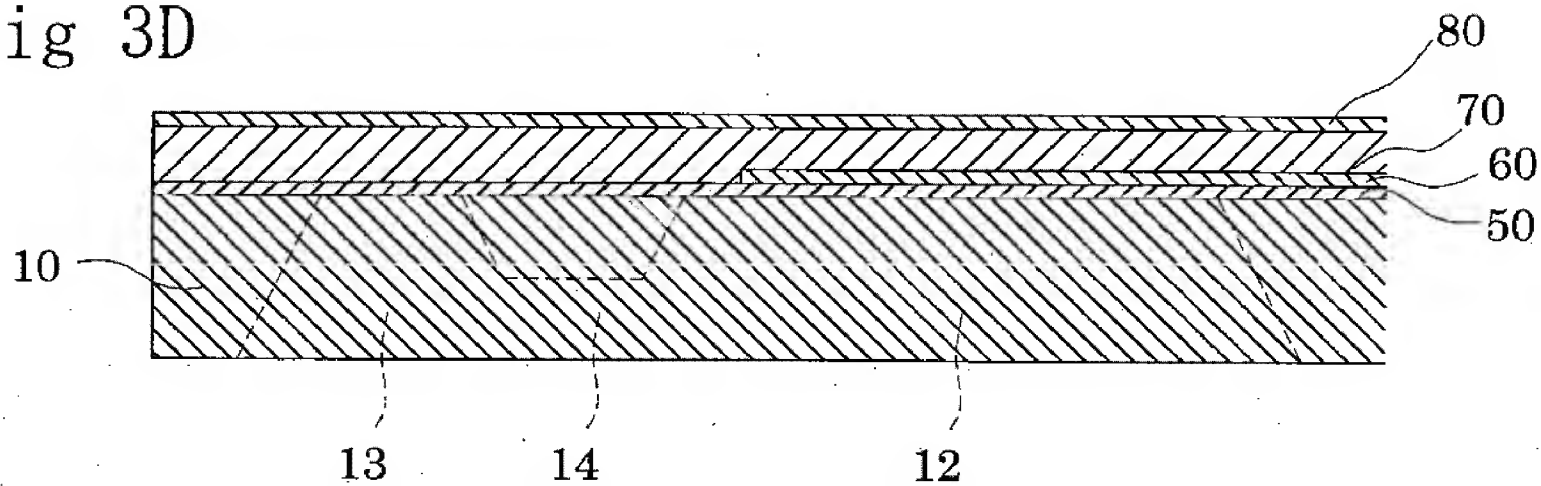


Fig 3D



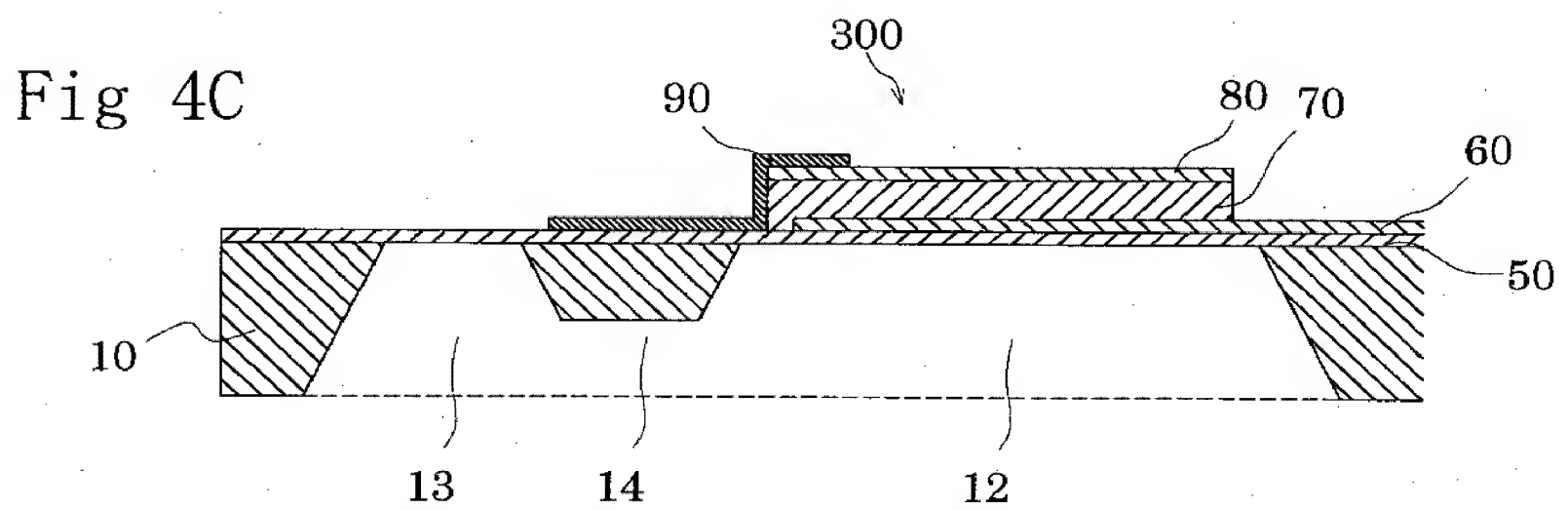
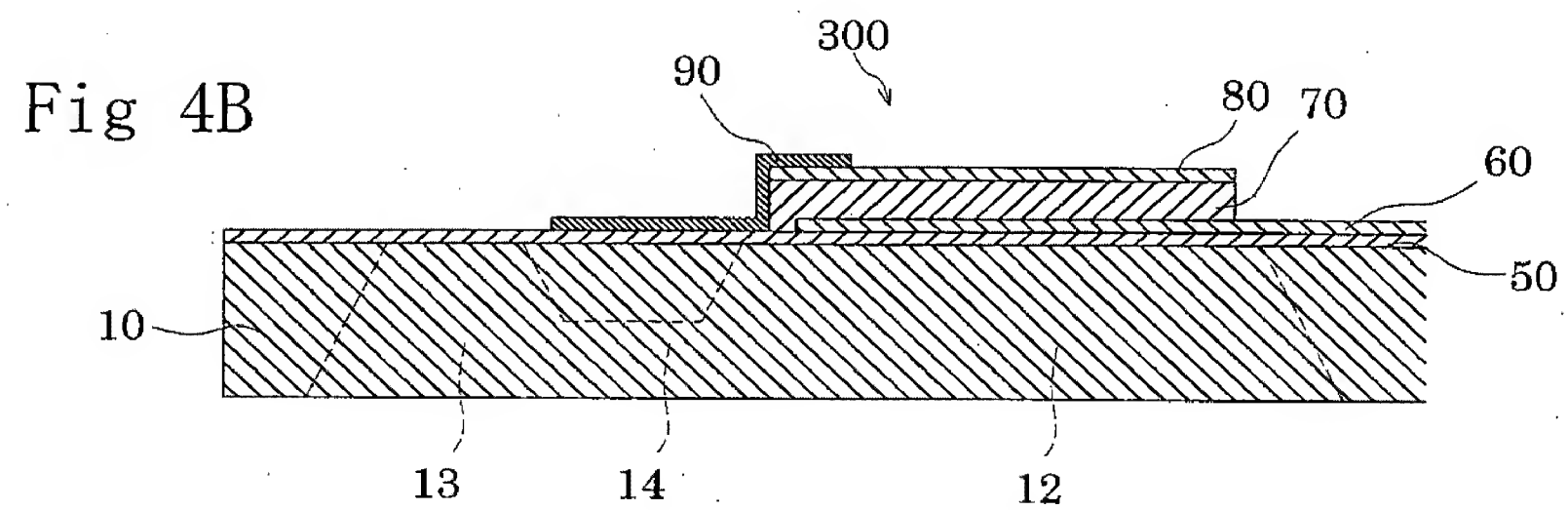
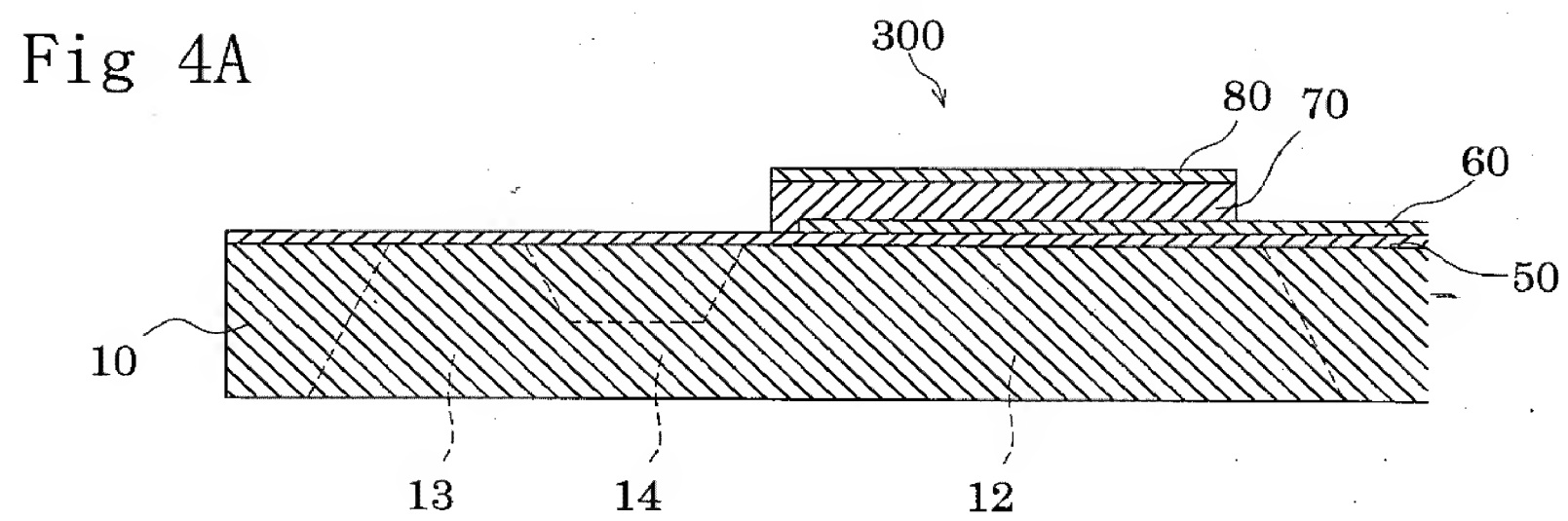






Fig 7A

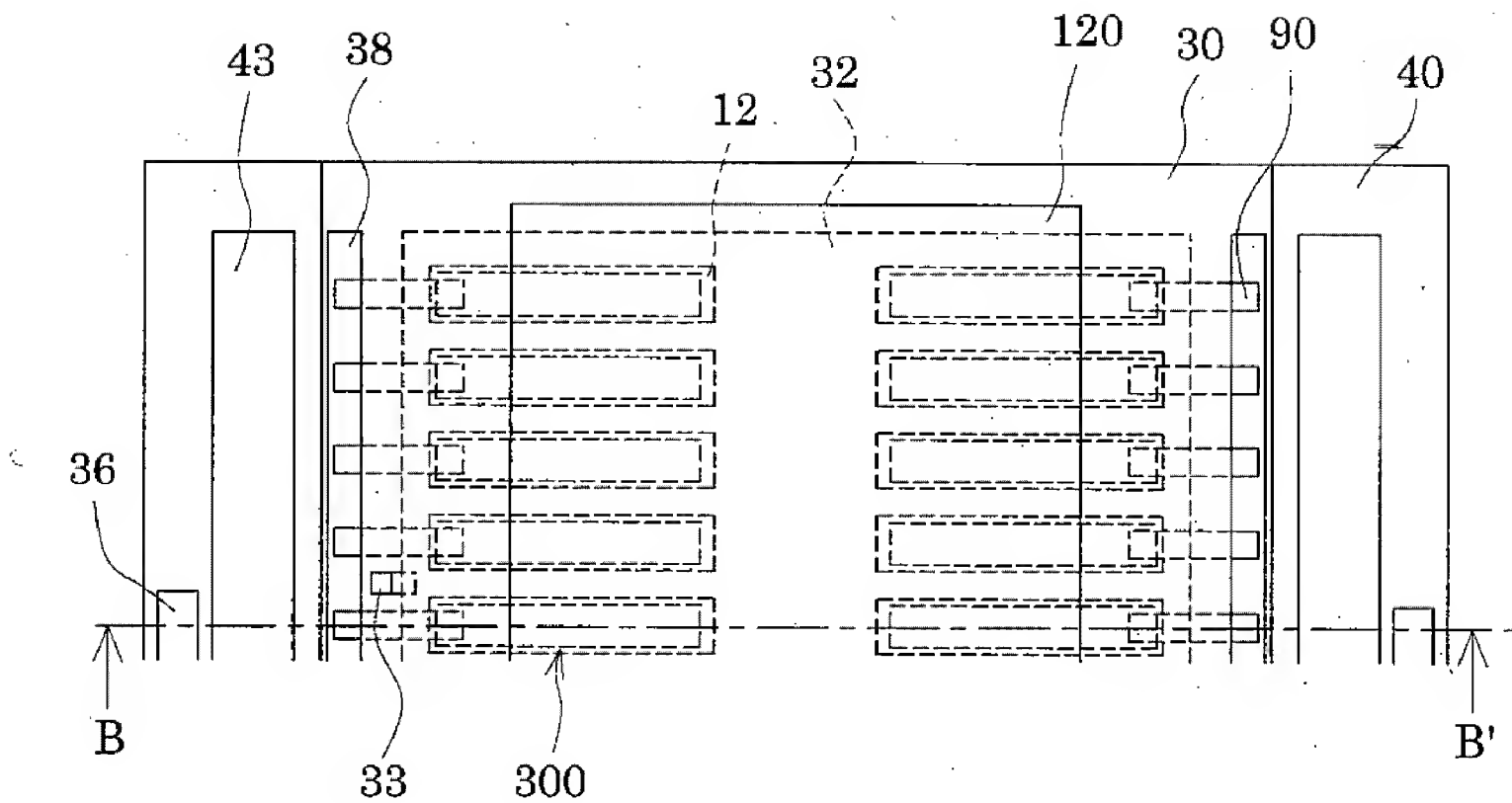


Fig 7B

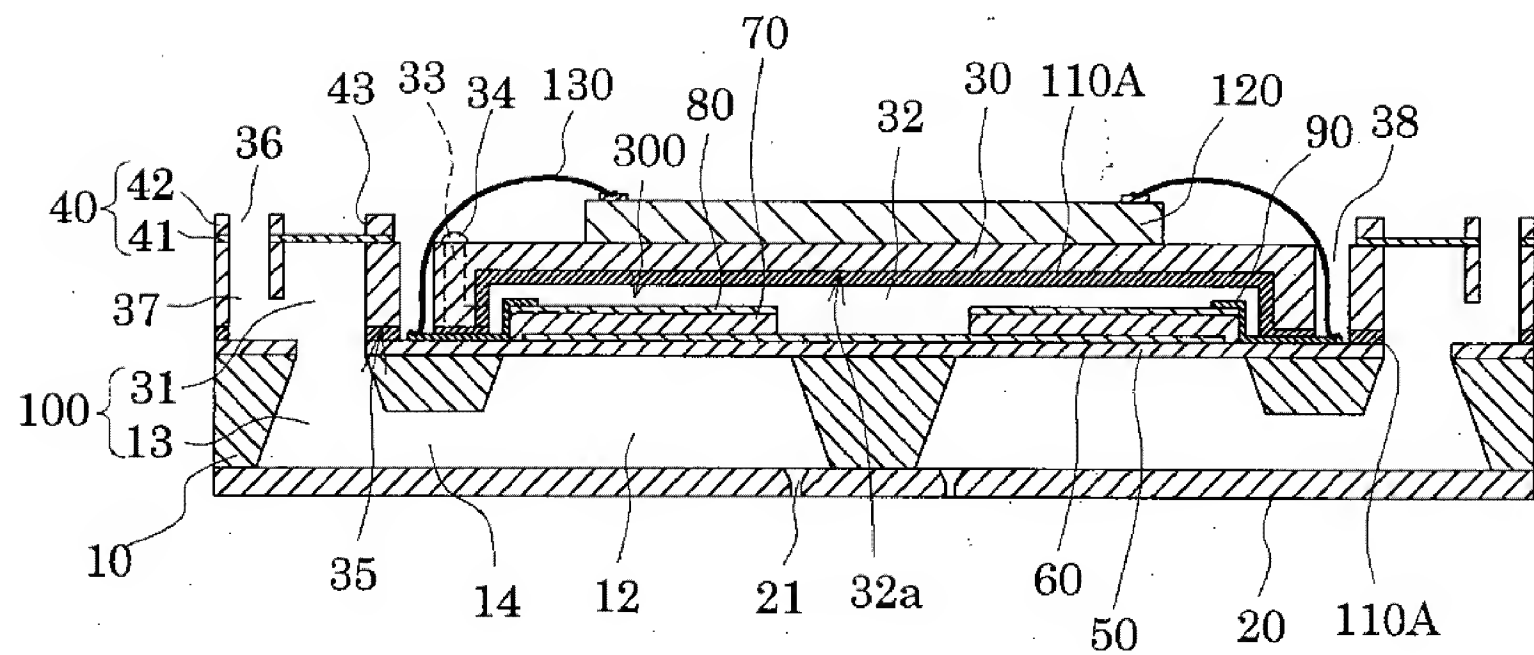


Fig 8B

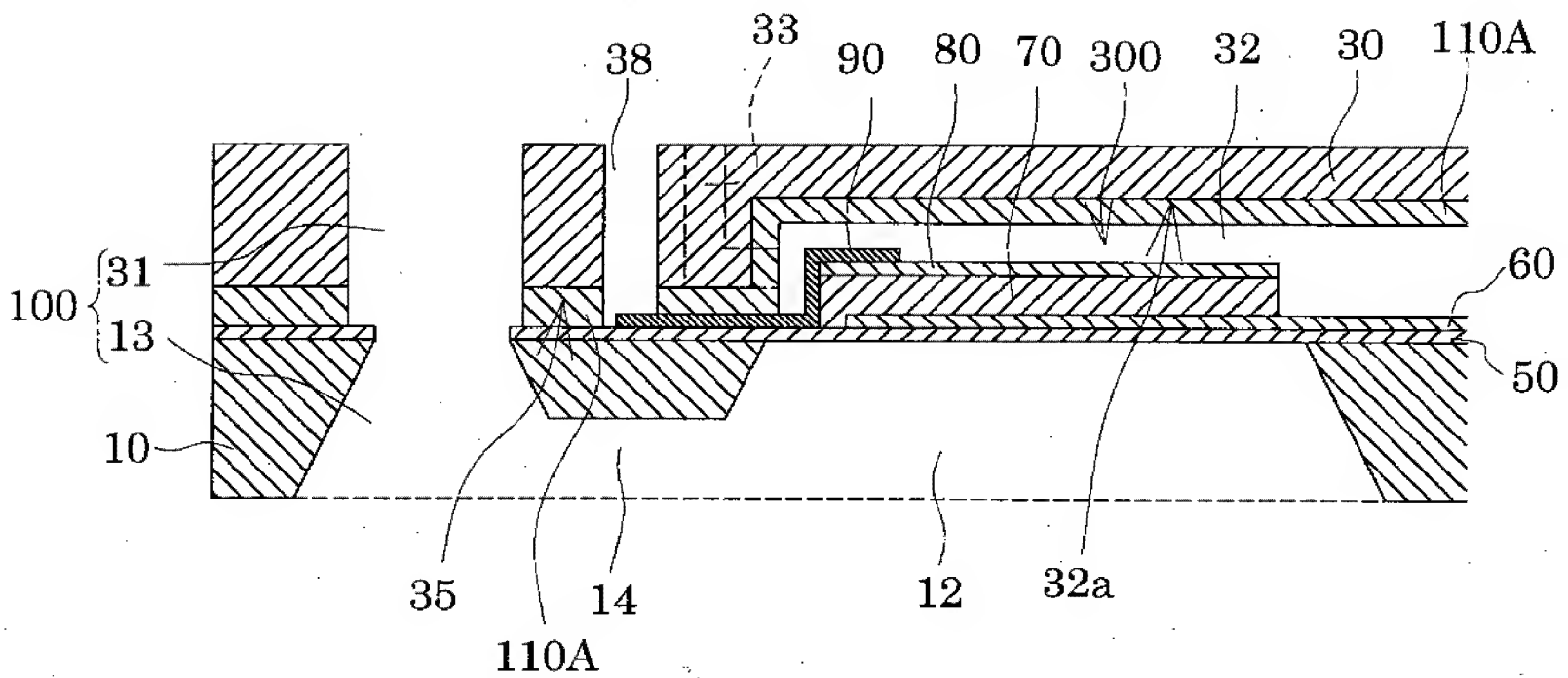




Diagram 110C is a cross-sectional view of a semiconductor device. It shows a substrate 30 with a gate stack 31 on top. The gate stack 31 includes a gate electrode 32 and a gate insulating layer 33. A gate insulating layer 35 is also shown. Arrows indicate the direction of light or radiation.

Fig 10A

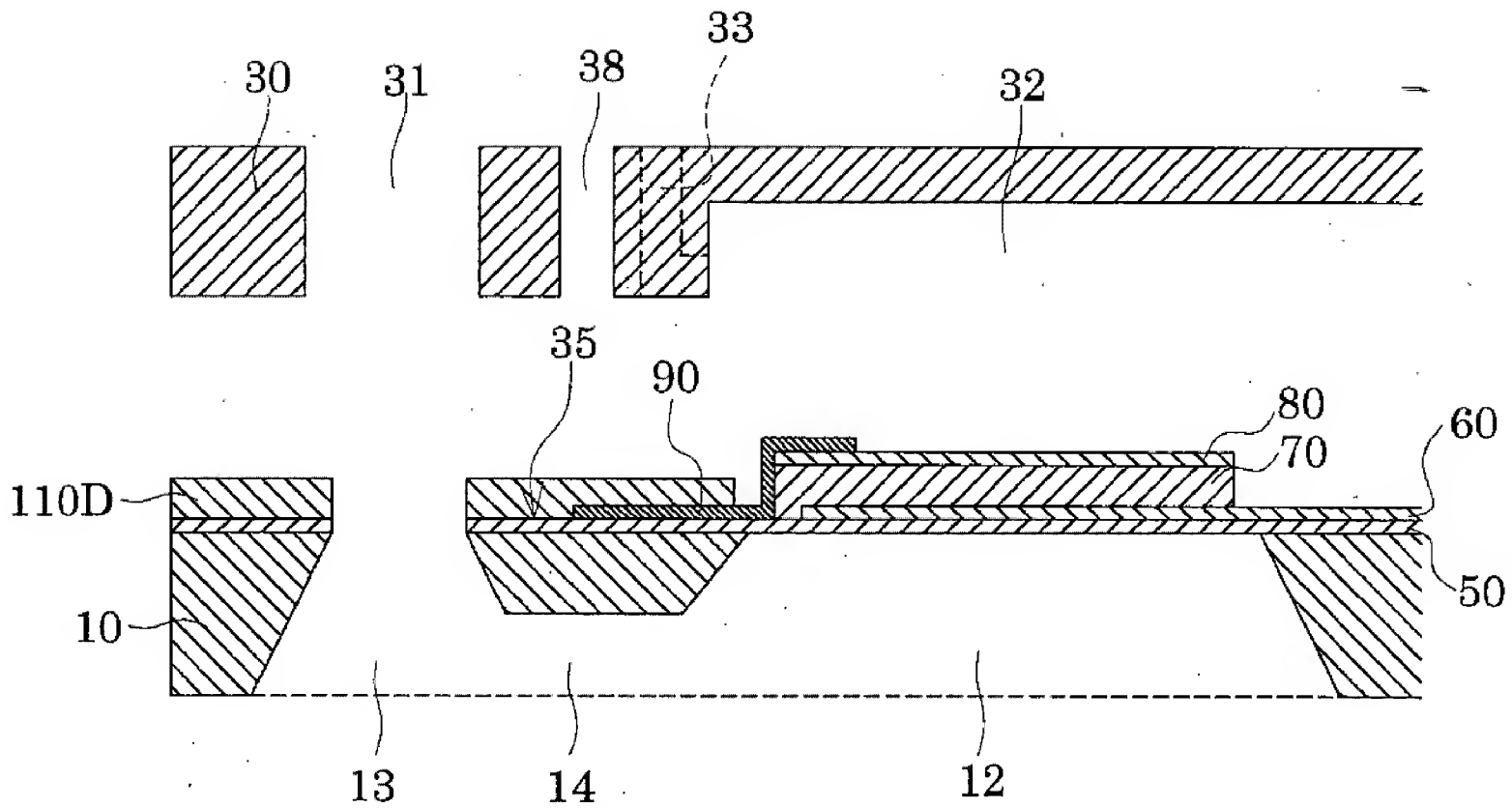


Fig 10B

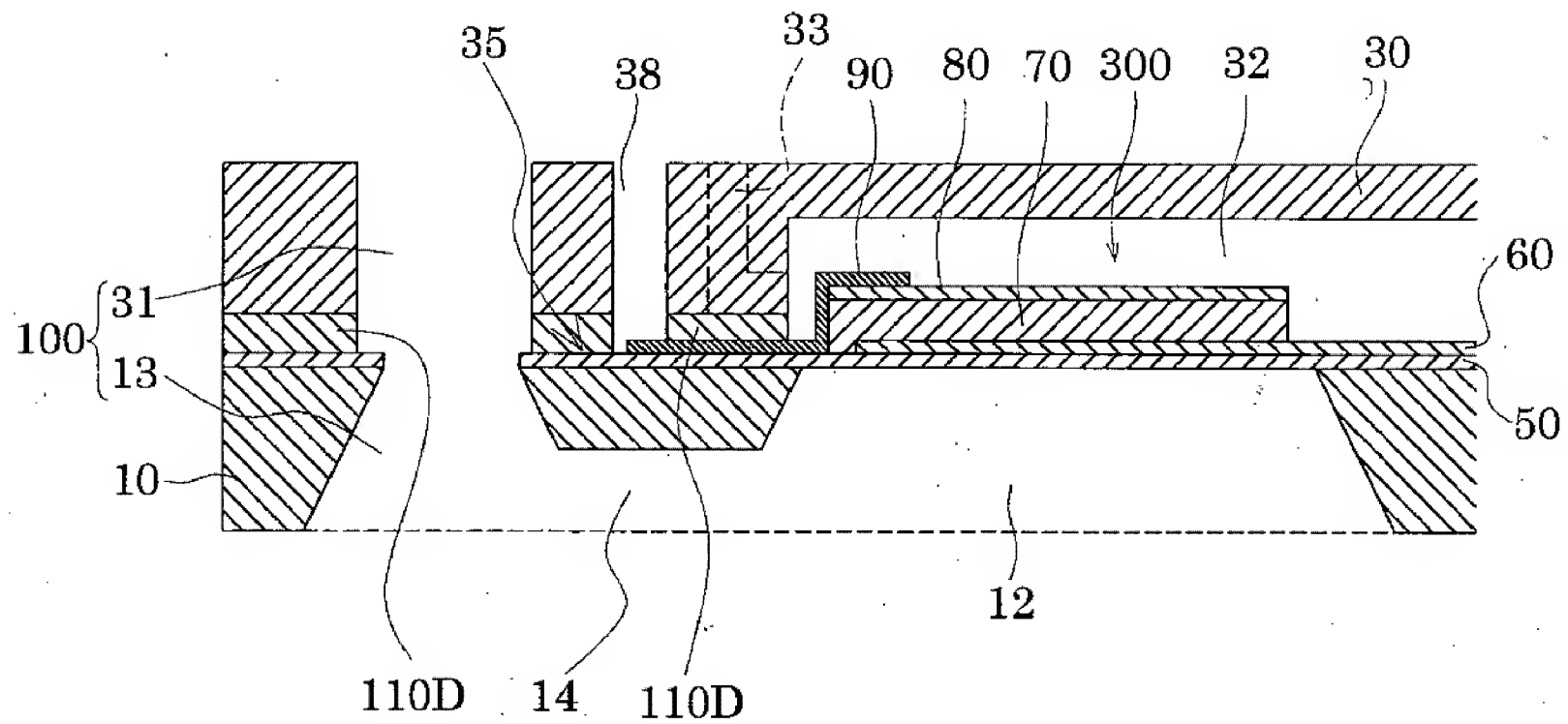


Fig 11A

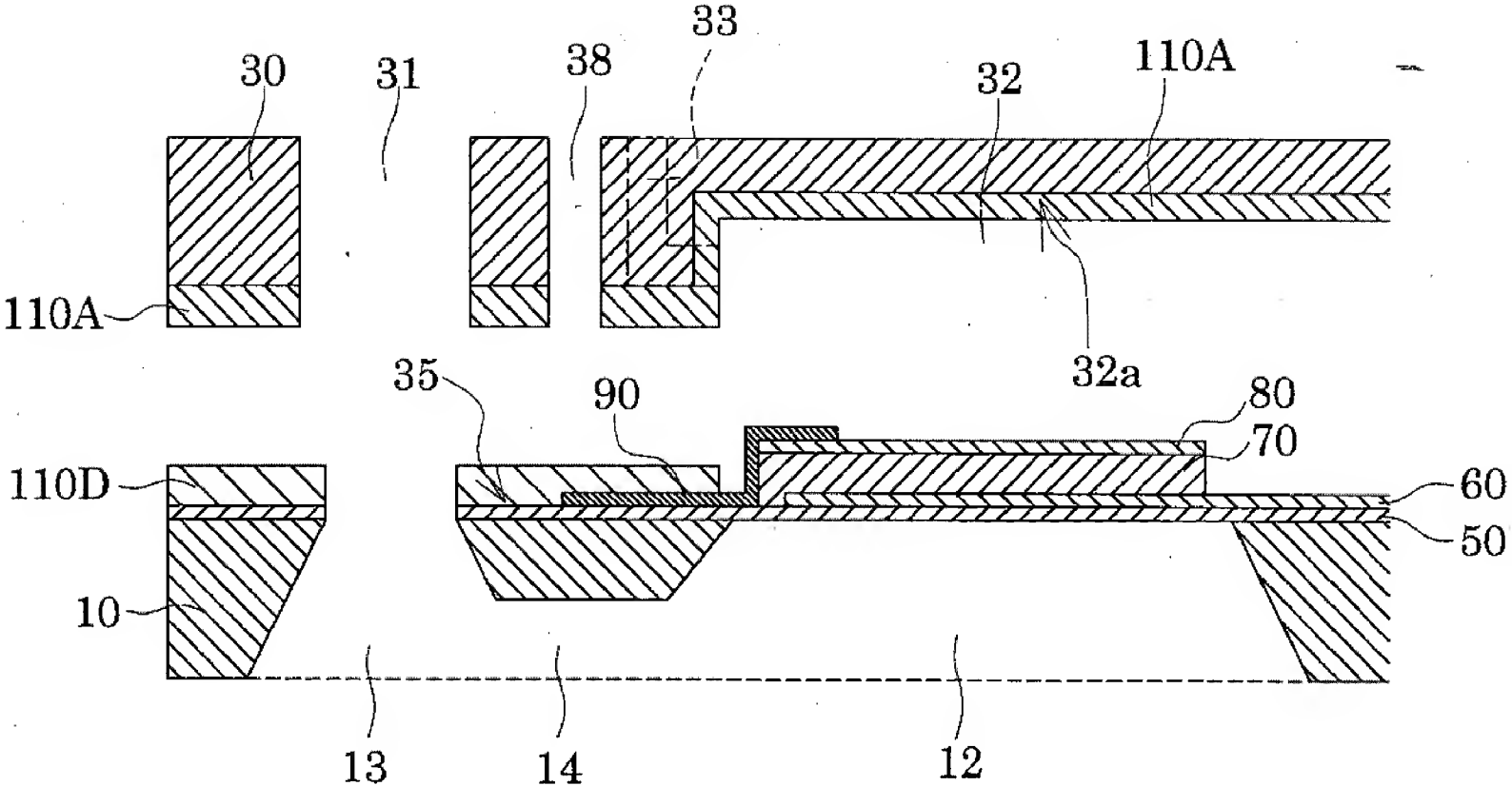


Fig 11B

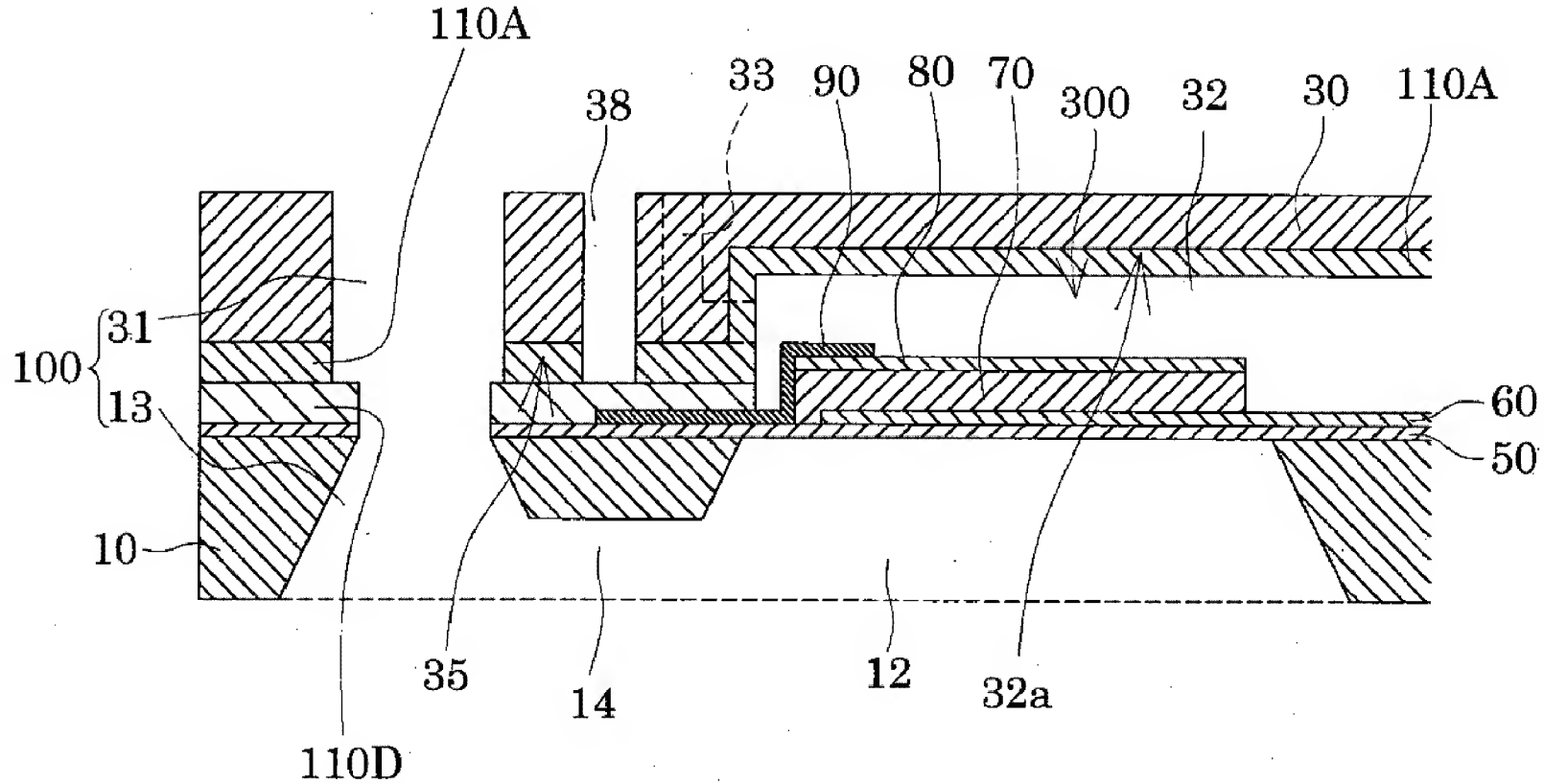


Fig 12A

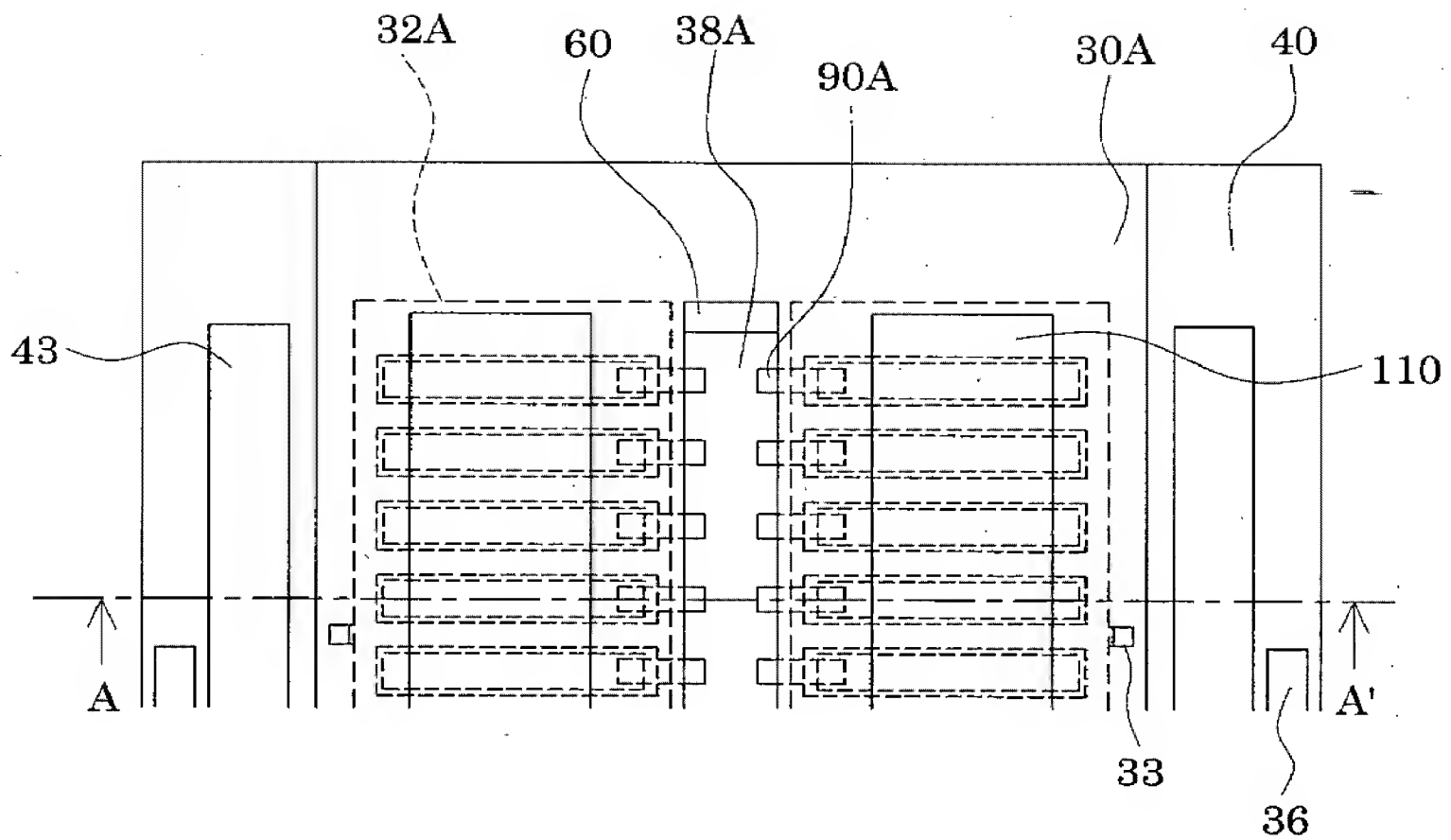


Fig 12B

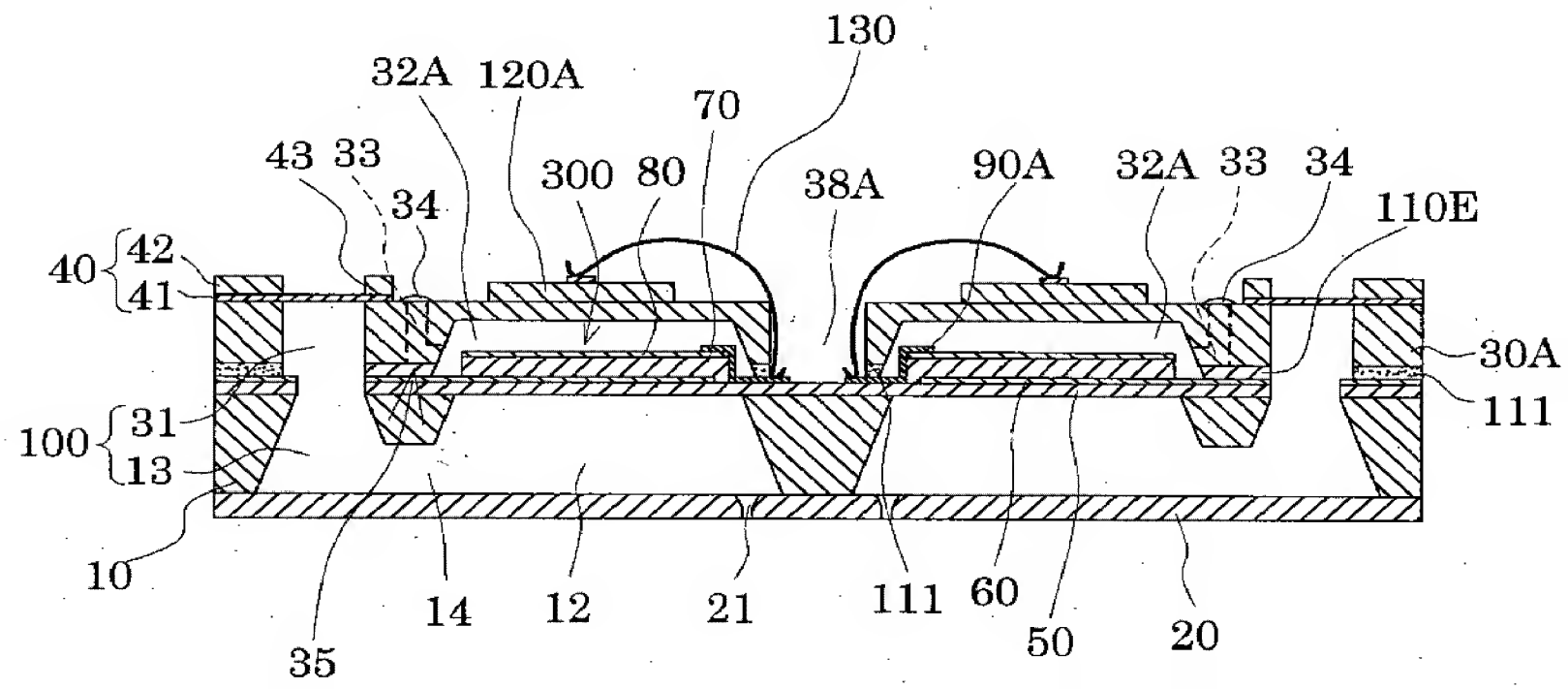


Fig 13

